

Top view; Pins facing downward; All dimensions are in mm; Header pitch of J1 & J2 is 1.27mm and for J3 is 2.54 mm. Drawing not to scale.

Motor - sensor configu	rations				
Motor Sensor	PMSM	BLDC	DC BRUSH	STEP (2-ph)	STEP (3-ph)
Incr. Encoder	T		•	9	
Incr. Encoder + Dig. Hall	T	T			
Linear Halls	7				
Digital Hall control only	T				
Analog Sin/Cos encoder	9	T	T	<b>(</b>	
SSI / BiSS-C/ EnDAT/ TAMAGAWA/ Panasonic/ Nikon/ Sanyo Denki	T	T	<b>a</b>	T	
Tacho	·		T		
Open-loop (no sensor)				<b>(</b>	<b>(</b>

- Features
- Motion controller and drive in a single compact unit based on MotionChip <sup>™</sup> technology
- Universal solution for control of rotary and linear brushless, brushed and 2 or 3-phase step motors
- Advanced motion control capabilities (PVT, S-curve, electronic cam)
- Motor supply: 11-50V; Logic SELV/ PELV supply: 9-36V; STO SELV/ PELV supply: 18-40V
- Output current: 10A¹ RMS cont. (BLDC mode); 28 A<sub>PEAK</sub> RMS, up to 100kHz PWM
- Operating ambient temperature: 0-40°C (over 40°C with derating)
- NTC/PTC analogue Motor Temperature sensor input
- Communication interfaces:
  - ■USB
  - ■RS232
  - dual 100Mbps EtherCAT® ports

- Feedback Devices (dual-loop support)
  - 1st feedback devices supported:
  - Incremental encoder interface (single ended or differential)
  - Analogue sin/cos encoder interface (differential 1V<sub>pp</sub>)
  - Digital Hall sensor interface (single-ended and open collector)
  - •Linear Hall sensors interface
  - pulse & direction interface (single ended or differential) for external (master) digital reference
  - 2<sup>nd</sup> feedback devices supported:
    - Incremental encoder interface (differential)
  - pulse & direction interface (differential) for external (master) digital reference
  - SSI / BiSS-C/ EnDAT/ TAMAGAWA/ Panasonic/ Nikon/ Sanyo Denki encoder interface
- STO: 2 safe torque-off inputs, safety integrity level (SIL3/Cat3/PLe) acc. to EN61800-5-1; -2/ EN61508-3; -4/ EN ISO 13849-1.
- 6 digital inputs, 12-36V, PNP/NPN programmable: 2 for limit
- switches, 4 general-purpose6 digital outputs, 5-36V, 0.5A, PNP/NPN programmable: Ready,
- Error, 4 general-purpose

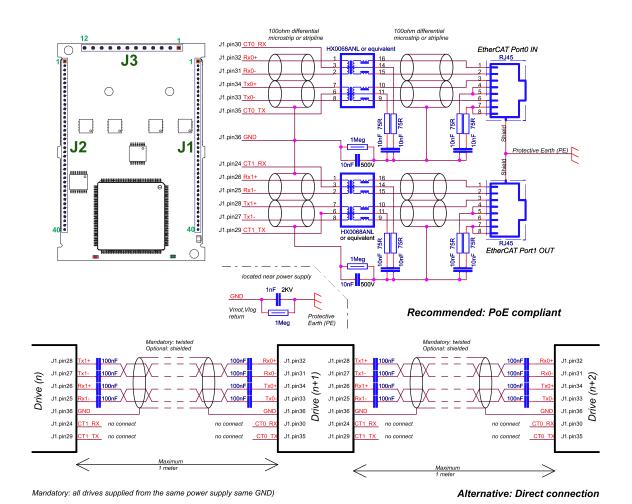
  2 analogue inputs: 12-bit, 0-5V: Reference, Feedback or general
- Commissioning (set-up) possible through RS232, FoE (file-over-EtherCAT®), EoE (Ethernet-over-EtherCAT®)
- EtherCAT⊚ connection between multiple MZ drives: direct 1:1 without any series components
- EtherCAT⊚ connection to standard RJ45: requires external magnetics (may be integrated into RJ45)
- 255 h/w addresses selectable by h/w pins configuration
- 16k x 16 SRAM memory for data acquisition
- 24k x16 E<sup>2</sup>ROM to store setup data, TML motion programs, cam tables and other user data

<sup>1</sup>Nominal current can be increased if external cooling is ensured over cooling area

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Mating Connectors						
When J3 is plugged into a connector and maximum current should not exceed 12.7A Sine amplitude						
Ref	Producer	Part No.	Description			
	Harwin	M52-5012045	1x20 contacts, socket 1.27mm-pitch; 4 pcs needed for one drive			
J1, J2	Samtec	SMS-140-01-L-S	1x40 contacts, socket 1.27mm-pitch; 2 pcs			
	Samec	SMS-140-01-G-S	needed for one drive			
J3	Mill-Max	801-47-012-10- 001000	1x12 contacts, High-current socket 2.54mm-pitch accepting 0.635mm square pin; 1 pc is needed for one drive; the current should not exceed 12.7A			
When		_	o a motherboard and the maximum d 13A Sine amplitude			
Ref	Producer	Part No.	Description			
J1, J2	Harwin	M52-5012045	1x20 contacts, socket 1.27mm-pitch; 4 pcs needed for one drive			
J3	The pins as capability	re directly soldered	onto a motherboard for increased current			

	Pin	Name	Type	Description
	1,2	GND	-	Return ground for motor. Internally connected to all GND signals except STO GND.
	3,4	Cr/B-	0	Chopping resistor / Phase B- for 2-ph steppers
	5,6	C/B+	0	Phase C for 3-ph motors, B+ for 2-ph steppers
73	7,8	B/A-	0	Phase B for 3-ph motors, A- for 2-ph steppers, Motor- for DC brush motors
	9,10	A/A+	0	Phase A for 3-ph motors, A+ for 2-ph steppers, Motor+ for DC brush motors
	11,12	+V <sub>MOT</sub>	ı	Positive terminal of the motor supply: 11 to 48V <sub>DC</sub> .

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	Pin	Name	Туре	Description
	1	Temp Mot	l l	NTC/PTC 3.3V input. Used to read an analog temperature value
	2	232TX	0	RS-232 Data Transmission
	3	232RX	I	RS-232 Data Reception
	4	USB Data-	I/O	USB Data negative
	5	USB Data+	I/O	USB Data positive
	6	USB V+	I	USB +5V input
	7	P1 LED	0	ECAT OUT port LED
	8	P0 LED	0	ECAT IN port LED
	9	reserved	-	
	10	Axis ID Bit6	I	8-bit H/W Axis ID register.
	11	Axis ID Bit5	- 1	Connect pin to GND to set bit to 1. Sets hardware Axis ID that is found in the
	12	Axis ID Bit4	I	_ECAT register configured station alias
	13	Axis ID Bit3	ı	Pin 16 is Bit 0Pin 10 is Bit 6 of the Axis value.
	14	Axis ID Bit2	<u> </u>	Possible values: from 1 to 127; and 255 when
	15	Axis ID Bit1	<u> </u>	Tall pins OFF; -When Axis ID is 255, the EtherCAT register
	16	Axis ID Bit0	I	called "configured station alias" will be 0.
	17	RUN	0	Anode of Run LED (EtherCAT status machine).
	18	ERR	0	Anode of Error LED (EtherCAT status machine).
	19	Spi2 Clk	0	Reserved. Do not use
	20	Spi2 Out	0	Reserved. Do not use
	21	Spi2 In	l l	Reserved. Do not use
	22	Spi2 CS	0	Reserved. Do not use
	23	Spi2 Irq		Reserved. Do not use
	24	CT1_Rx	-	Connect to center tap of OUT port magnetics PHY Rx.
_	25	RX1-	I/O	Receive/Transmit negative, OUT port. Connect to magnetics PHY RX1.
5	26	RX1+	I/O	Receive/Transmit positive, OUT port. Connect to magnetics PHY RX1.
	27	TX1-	I/O	Transmit/Receive negative, OUT port. Connect to magnetics PHY TX1.
	28	TX1+	I/O	Transmit/Receive positive, OUT port. Connect to magnetics PHY TX1.
	29	CT1_Tx	-	Connect to center tap of OUT port magnetics PHY Tx.
	30	CT0_Rx	-	Connect to center tap of IN port magnetics PHY Rx.
	31	RX0-	I/O	Receive/Transmit negative, IN port. Connect to magnetics PHY RX0.
	32	RX0+	I/O	Receive/Transmit positive, IN port. Connect to magnetics PHY RX0.
	33	TX0-	I/O	Transmit/Receive negative, IN port. Connect to magnetics PHY TX0.
	34	TX0+	I/O	Transmit/Receive positive, IN port. Connect to magnetics PHY TX0.
	35	СТ0_Тх	-	Connect to center tap of IN port magnetics PHY Tx.
	36	GND	-	Return ground. Internally connected to all GND signals except STO GND.
	37	STO2-	I	Safe Torque Off input 2, negative return (opto-isolated, 0V)
	38	STO2+	I	Safe Torque Off input Apply between both 2, positive input (opto-isolated, 18+40V) STO1-, STO2- 24V DC
	39	STO1-	ı	Safe Torque Off input from SELV/ PELV 1, negative return power supply for motor (opto-isolated, 0V) PWM output operation
	40	STO1+	I	Safe Torque Off input 1, positive input (opto- isolated, 18÷40V)

	Pin	Name	Туре	Description
	1	LH1	T	Linear Hall 1 input
	2	LH2	Т	Linear Hall 2 input
	3	LH3	1	Linear Hall 3 input
				Analogue input, 12-bit, 0-5V. Reads analogue
	4	FDBK	ı	feedback (tacho), or general purpose
	5	REF	- 1	Analogue input, 12-bit, 0-5V. Reads analog reference,
				or general-purpose analogue input
	6	Hall 3		Digital input Hall 3 sensor
	7	Hall 2	_ !	Digital input Hall 2 sensor
	8	Hall 1		Digital input Hall 1 sensor
	9	GND	-	Return ground. Internally connected to all GND signals except STO GND.
	10	IN5		12-36V general-purpose digital PNP/NPN input
	11	IN4	Ī	12-36V general-purpose digital PNP/NPN input
	12	IN1	1	12-36V general-purpose digital PNP/NPN input
	13	IN0	1	12-36V general-purpose digital PNP/NPN input
		1110// 00		12-36V digital PNP/NPN input. Positive limit switch
	14	IN2/LSP	ı	input
	15	IN3/LSN	- 1	12-36V digital PNP/NPN input. Negative limit switch
				input  5.36\/ general purpose digital output, 0.2A DND/ 0.2A
	16	OUT3	0	5-36V general-purpose digital output, 0.2A PNP/ 0.3A NPN, software selectable
				5-36V general-purpose digital output, 0.2A PNP/ 0.3A
	17	OUT2	0	NPN, software selectable
	18	OUT5	0	5-36V general-purpose digital output, 0.2A PNP/
		0010		0.3A NPN, software selectable
	19	OUT4	0	5-36V general-purpose digital output, 0.2A PNP/ 0.3A NPN, software selectable
				5-36V general-purpose digital output, 0.2A PNP/
	20	OUT1	0	0.3A NPN, software selectable
	21	OUT0	o	5-36V general-purpose digital output, 0.2A PNP/ 0.3A
				NPN, software selectable
72	22	Z1+	<u> </u>	Incr. encoder1 Z single-ended, or Z+ diff. input,
	23	Z1-	l	Incr. encoder1 Z- diff. input Incr. encoder1 B single-ended, or B+ diff. input, or
	24	B1+/Cos+	ı	analogue encoder Cos+ diff. input
	^-	D4 (0)		Incr. encoder1 B- diff. input, or analogue encoder
	25	B1-/Cos-	ı	Cos- diff. input
	26	A1+/Sin+	1	Incr. encoder1 A single-ended, or A+ diff. input, or
				analogue encoder Sin+ diff. input Incr. encoder1 A- diff. input, or analogue encoder Sin-
	27	A1-/Sin-	- 1	diff. input
	28	Z2+	ī	Incr. encoder2 Z+ diff. input; has 150Ω resistor
	20	22+		between pins 28 and 29
	29	<b>Z</b> 2-	- 1	Incr. encoder2 Z- diff. input; has $150\Omega$ resistor
				between pins 28 and 29 Incr. encoder2 B- diff. input, or Dir, or Clock- for SSI,
	30	B2-/Dir-	I/O	or Master- for BiSS; has $150\Omega$ resistor between pins
		/CLK-/MA-		30 and 31
		B2+/Dir+/	.,,	Incr. encoder2 B+ diff. input, or Dir+-, or Clock+ for
	31	CLK+/MA+	I/O	SSI, or Master+ for BiSS; has $150\Omega$ resistor between pins 30 and 31
				Incr. encoder2 A+ diff. input, or Pulse+, or Data+ for
	32	A2+/Pulse+ / Data+/SL+	- 1	SSI, or Slave+ for BiSS; has $150\Omega$ resistor between
		, DalaT/SLT		pins 32 and 33
	22	A2- /Pulse-/		Incr. encoder2 A- diff. input, or Pulse-, or Data- for
	33	Data-/SL-	ı	SSI, or Slave- for BiSS; has $150\Omega$ resistor between pins 32 and 33
	34	Reserved	-	Reserved. Do not use
	35	Reserved	-	Reserved. Do not use
	36	Reserved	-	Reserved. Do not use
	37	Reserved	-	Reserved. Do not use
	38	+5V <sub>OUT</sub>	0	5V output supply for I/O usage
	39		ı	Negative terminal of the logic supply input: 9 to 36V <sub>DC</sub>
	39	-V <sub>LOG</sub>		from SELV/ PELV type power supply.
	40	+V <sub>LOG</sub>	1	Positive terminal of the logic supply input: 9 to 36V <sub>DC</sub> from SELV/ PELV type power supply
	-			from SELV/ PELV type power supply.

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#### **Electrical characteristics**

All parameters measured under the following conditions (unless otherwise specified):

- VLOG = 24 VDC; VMOT = 48VDC
- Supplies start-up / shutdown sequence: -any-Load current (sinusoidal amplitude / cont. BLDC, DC, stepper) = 10A RMS

		solual amp	olitude / cont. E				
Operating Conditi				Min.	Тур.	Max.	Units
Ambient temperatu	re	-		0		40 <sup>1</sup>	°C
Ambient humidity		Non-cond		0	0	90	%Rh
Altitude / pressure	2		s. sea level)	-0.1	0 ÷ 2.5	10.0	Km
·		Ambient F	ressure	0 2	0.75 ÷ 1	10.0	atm
Storage Condition	IS			Min.	Тур.	Max.	Units
Ambient temperatu	re			-40		100	°C
Ambient humidity		Non-cond	ensing	0		100	%Rh
Ambient Pressure				0		10.0	atm
ESD capability			ed; applies to			±0.5	kV
(Human body mod	el)	any acces Original pa				±15	kV
Machaniaal Marra	41	Original pa	ackaging	Min	T		
Mechanical Moun	ung			Min.	Typ. al convecti	Max.	Units
	irflow  Between adjacent driv			30	ai convecti	on , close	mm
Spacing required	Between drives and nearby						
for vertical mounting	wall			30			mm
mounting			s and roof-top	20			mm
		ween adjac		4			mm
Spacing required	Bet		s and nearby	5			mm
for horizontal		ce needed	for drive				
mounting		oval		10	<u> </u>		mm
			s and roof-top	15			mm
Insertion force			ended mating	TDD	TBD	TBD	N
Extraction force		nectors		TBD	TBD		N
Environmental Ch	arac	teristics		Min.	Typ.	Max.	Units
Size (Length x Width x Height)	Glo	bal size			x 43.6 x 1 2 x 1.72 x		mm
Weight	_			~2.5	Z X 1.72 X TBD	0.04	inch g
	Drv	cleaning is	i				
Cleaning agents	reco	mmended		Only Water- or Alcohol- based			
Protection degree	Acc UL5	ording to IE 108	EC60529,	IP20			-
Power dissipation			ent, 20KHz		TBD		Watt
		ninal	EtherCAT		TBD		%
Global efficiency	curi 20k	ent, (Hz	CAN bus		TBD		%
Logic Supply Inpu				Min.	Тур.	Max.	Units
		ninal values		9		36	V <sub>DC</sub>
Supply wells	driv gua	olute maxir e operating ranteed pa	8		40	V <sub>DC</sub>	
Supply voltage	con	tinuous	num values,	-0.6		42	V <sub>DC</sub>
	surg	ge (duration	mum values, n ≤ 10ms) <sup>†</sup>	-1		+45	V
		og = 12V			150		↓
Supply current		og = 24V			100		mA
Motor Complete		og = 40V		Min	80 Tum	Man	Units
Motor Supply Inp		<b>/</b> мот) ninal value:		<b>Min.</b> 11	Тур.	<b>Max.</b> 50	Units V <sub>DC</sub>
	_		num values,	11		30	V DC
	driv gua	e operating ranteed pa	but outside rameters	9		52	V <sub>DC</sub>
Supply voltage	Abs	olute maxir tinuous	num values,	-0.6		54	V <sub>DC</sub>
		olute maxir ge (duratior	mum values, n ≤ 10ms) <sup>†</sup>	-1		57	V
	Idle				1	5	mA
		erating		-40	±10	+40	Α
Supply current	sho	rt-circuit co				43	А
	(du	ation ≤ 10r	ns) ˈ				<u> </u>

Supply Output (+			Min.	Тур.	Max.	Units
Output voltage	Current sourced = :	250mA	4.8	5	5.2	V
Output current				TBD	l	mA
Short-circuit	-			NOT pr		
Over-voltage ESD protection	Lluman hadu mada		.1	NOT pr	otected	1417
Isolation PE (ear	Human body mode	:1	±1		±250	kV
	(II) - GND (A+, B/A-, C/B+, CR	ND \	Min.	Turn	Max.	Units
wotor Outputs (A		,	WIII.	Тур.	Wax.	Units
Nominal output	for DC brushed, st and BLDC motors based trapezoidal	with Hall- control			14.3	
current, continuous <sup>4</sup>	for PMSM motors sinusoidal control amplitude value)	(sinusoidal			14.3	А
	for PMSM motors with FOC sinusoidal control (sinusoidal effective value)				10	
Motor output current, peak	maximum TBD s		-40		+40	Α
Short-circuit protection threshold			±43		±43	Α
Short-circuit protection delay				TBD		μs
On-state voltage drop	Nominal output cuincluding typical m	ating		TBD		٧
Voltage efficiency	Someon comact	. solotarioc		100		%
Off-state leakage						
current				±0.5	±1	mA
	Recommended	F <sub>PWM</sub>				
	value, for current	20 kHz	400			4
	ripple max. ±5% of	40 kHz	200			μН
	full range; +V <sub>MOT</sub> = 36 V	OU KHZ	150			μιι
Motor inductance		80 kHz	100			
(phase-to-phase)		100 kHz	80			
,	Minimum value, limited by short- circuit	20 kHz	150			-
		60 kHz	50			1
		40 kHz	40			<u>μ</u> Η
	protection; +V <sub>MOT</sub> = 36 V	80 kHz	20			
		100 kHz	10			
Motor electrical	Recommended value for ±5%	20 kHz 40 kHz	330 170			1
time-constant	current	60 kHz	140			116
(L/R)	measurement	80 kHz	80			μs
(2/14)	error	100 kHz	66			1
Current			- 00	TDD		0/50
measurement	FS = Full Scale ac	curacy		TBD		%FS
Digital Hall Inputs	s (Hall1, Hall2, Hall3	3)	Min.	Тур.	Max.	Units
Mode compliance		,	TTL		Open-col	lector
Default state	Input floating		TTL / CMOS / Open-collector  Logic HIGH			
Delault State	(Wiring disconnect	ted)		Logic	HIGH	
Delault State	(Wiring disconnect Logic "LOW"	ted)		Logic 0	0.8	]
Delault State	Logic "LOW" Logic "HIGH"	ted)	2			
	Logic "LOW" Logic "HIGH" Floating voltage (Not connected)	·	2	0		V
Input voltage	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s)	n, surge	-10	0 5		V
Input voltage	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s) Logic "LOW"; Pull	n, surge to GND		0 5	0.8	V
Input voltage	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s)	n, surge to GND		0 5	+15	V
Input voltage Input current Minimum pulse	Logic "LOW"  Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s)  Logic "LOW"; Pull Logic "HIGH"; Inte	n, surge to GND	-10	0 5 4.4	+15 1.2	
Input voltage  Input current  Minimum pulse width	Logic "LOW"  Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s)  Logic "LOW"; Pull Logic "HIGH"; Inte	n, surge to GND rnal 4.7KΩ	-10	0 5 4.4	+15 1.2	mA
Input voltage  Input current  Minimum pulse width  ESD protection	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s) Logic "LOW"; Pull Logic "HIGH"; Inte pull-up to +5	n, surge to GND rnal 4.7KΩ	-10 0 2 ±5	0 5 4.4	+15 1.2 0	mA μs kV
Input voltage Input current Minimum pulse width ESD protection Linear Hall Inputs	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s) Logic "LOW"; Pull Logic "HIGH"; Inte pull-up to +5  Human body mode s (LH1, LH2, LH3)	n, surge to GND rnal 4.7KΩ	-10 0 2	0 5 4.4 0	+15 1.2	mA μs
Input voltage  Input current  Minimum pulse width  ESD protection  Linear Hall Inputs  Input voltage	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s) Logic "LOW"; Pull Logic "HIGH"; Inte pull-up to +5  Human body mode s (LH1, LH2, LH3) Operational range Absolute maximum continuous	n, surge to GND mail 4.7KΩ	-10 0 2 ±5 Min.	0 5 4.4	0.8 +15 1.2 0	mA μs kV <b>Units</b> V
Input voltage  Input current  Minimum pulse width  ESD protection  Linear Hall Inputs  Input voltage	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s) Logic "LOW"; Pull Logic "HIGH"; Inte pull-up to +5  Human body mode s (LH1, LH2, LH3) Operational range Absolute maximum continuous Absolute maximum,	n, surge to GND mail 4.7KΩ	-10  0  2  ±5  Min.  0	0 5 4.4 0	0.8 +15 1.2 0 <b>Max.</b> 4.9	mA μs kV <b>Units</b>
Input voltage  Input current  Minimum pulse width  ESD protection  Linear Hall Inputs  Input voltage	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s) Logic "LOW"; Pull Logic "HIGH"; Inte pull-up to +5  Human body mode s (LH1, LH2, LH3) Operational range Absolute maximum continuous Absolute maximum (duration ≤ 1s)	n, surge to GND rnal 4.7ΚΩ el	-10  0  2  ±5  Min.  0  -7  -11	0 5 4.4 0	0.8 +15 1.2 0 Max. 4.9 +7 +14	mA μs kV Units V
Input voltage  Input current  Minimum pulse width  ESD protection	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s) Logic "LOW"; Pull Logic "HIGH"; Inte pull-up to +5  Human body mode s (LH1, LH2, LH3) Operational range Absolute maximum continuous Absolute maximum,	n, surge to GND rnal 4.7KΩ el values, surge	-10  0  2  ±5  Min.  0  -7	0 5 4.4 0	0.8 +15 1.2 0 <b>Max.</b> 4.9 +7 +14	mA  μs  kV  Units  V  mA
Input voltage  Input current  Minimum pulse width  ESD protection  Linear Hall Inputs  Input voltage  Input voltage	Logic "LOW" Logic "HIGH" Floating voltage (Not connected) Absolute maximun (duration ≤ 1s) Logic "LOW"; Pull Logic "HIGH"; Inte pull-up to +5  Human body mode s (LH1, LH2, LH3) Operational range Absolute maximum continuous Absolute maximum, (duration ≤ 1s) Input voltage 0+5	n, surge to GND rnal 4.7KΩ el values, surge	-10  0  2  ±5  Min.  0  -7  -11	0 5 4.4 0	0.8 +15 1.2 0 Max. 4.9 +7 +14	mA  μs kV  Units  V

<sup>&</sup>lt;sup>1</sup>Operating temperature at higher temperatures is possible with reduced current and power ratings <sup>2</sup> iPOS4810 can be operated in vacuum (no altitude restriction), but at altitudes over 2,500m, current and power rating are reduced due to thermal dissipation efficiency.

 $<sup>^3</sup>$  In case of forced cooling (conduction or ventilation) the spacing requirements may drop substantially down to zero as long as the ambient temperature is kept below the maximum operating limit  $^4$  @20kHz  $\rm F_{PWM}$ 

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Digital Inputs (IN0, IN1, IN2/LSP	, IN3/LSN, IN4, IN5, IN6) <sup>1</sup>	Min.	Тур.	Max.	Units
Mode compliance			F	PNP	1
Default state	Input floating (wiring disconnected)		Logi	ic LOW	
	Logic "LOW"	-10	0	2.2	
	Logic "HIGH"	6.3	24	36	
	Hysteresis	1.2	2.4	2.8	
	Floating voltage (not		0		
Input voltage	connected)		U		V
	Absolute maximum,	-10		+39	
	Absolute maximum, surge				
	(duration ≤ 1s)	-20		+40	
	Logic "LOW"; pulled to GND		0		
Input current	Logic "HIGH"		8	10	mA
Mode compliance			١	NPN	
Default state	Input floating (wiring		Logi	c HIGH	
Delault State	disconnected)		Logi		
	Logic "LOW"		0	2.2	
	Logic "HIGH"	6.3	24	36	
	Hysteresis	1.2	2.4	2.8	
Input voltage	Floating voltage (not		15		V
, ,	connected)		15		
	Absolute maximum,	-10		+39	
	continuous				
	Absolute maximum, surge	-20		+40	
	(duration ≤ 1s) <sup>†</sup>				
	Logic "LOW"; Pulled to GND		8	10	
Input current	Logic "HIGH"; Pulled to +24V	0	0	0	mA
	Logic Thorr, rulled to 124V		U	_	1.11-
Input frequency		6		10	kHz
Minimum pulse ESD protection	Human body model				µs k\/
ESD protection  Encoder1 Inputs	Human body model	±5	T	Mari	kV
ESD protection Encoder1 Inputs (A1/A1+, A1-, B1/B	31+, B1-, Z1/Z1+, Z1-)		Тур.	Max.	_
ESD protection Encoder1 Inputs (A1/A1+, A1-, B1/E Single-ended	31+, B1-, Z1/Z1+, Z1-) Leave negative inputs	±5 Min.			kV Units
ESD protection Encoder1 Inputs (A1/A1+, A1-, B1/B	31+, B1-, Z1/Z1+, Z1-) Leave negative inputs disconnected	±5 Min.		/ Open-co	kV Units
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/E Single-ended mode compliance Input voltage,	S1+, B1-, Z1/Z1+, Z1-) Leave negative inputs disconnected Logic "LOW"	±5 Min.			kV Units
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/E Single-ended mode compliance Input voltage, single-ended	31+, B1-, Z1/Z1+, Z1-) Leave negative inputs disconnected Logic "LOW" Logic "HIGH"	±5 Min.	CMOS	/ Open-co	kV Units
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/E Single-ended mode compliance Input voltage,	B1+, B1-, Z1/Z1+, Z1-) Leave negative inputs disconnected Logic "LOW" Logic "HIGH" Floating voltage (not connected)	±5 Min.		/ Open-co	kV Units
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/E Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+	B1+, B1-, Z1/Z1+, Z1-) Leave negative inputs disconnected Logic "LOW" Logic "HIGH" Floating voltage (not connected) Logic "LOW"	±5 Min. TTL /	CMOS	/ Open-co	kV Units
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/E Single-ended mode compliance Input voltage, single-ended	31+, B1-, Z1/Z1+, Z1-) Leave negative inputs disconnected Logic "LOW" Logic "HIGH" Floating voltage (not connected) Logic "LOW" Logic "LOW" Logic "HIGH"	±5 Min.	CMOS	/ Open-co	kV Units
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/It Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage,	31+, B1-, Z1/Z1+, Z1-) Leave negative inputs disconnected Logic "LOW" Logic "HIGH" Floating voltage (not connected) Logic "LOW" Logic "HIGH" Floating voltage (not	±5 Min. TTL /	CMOS	/ Open-co	kV Units  bllector
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/E Single-ended mode compliance  Input voltage, single-ended mode A/A+, B/B+  Input voltage, single-ended mode Z/Z+	B1+, B1-, Z1/Z1+, Z1-) Leave negative inputs disconnected Logic "LOW" Logic "HIGH" Floating voltage (not connected) Logic "LOW" Logic "HIGH" Floating voltage (not connected)	±5 Min. TTL /	CMOS	/ Open-co	kV Units  bllector
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/E Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended	B1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "HIGH"	±5 Min. TTL /	3.3 4.7	/ Open-co	kV Units  Dilector  V
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended d Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+,	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ	±5 Min. TTL /	3.3 4.7	/ Open-co	kV Units  bllector
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+	B1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5	±5 Min.  TTL 4  1.8	3.3 4.7 5.5	/ Open-cc 1.6 1.2 6 0	kV Units  Dilector  V
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended d Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+,	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ	±5 Min.  TTL 4  1.8	3.3 4.7 5.5	/ Open-co	kV Units  Dilector  V
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance,	±5 Min.  TTL 4  1.8	3.3 4.7 5.5	/ Open-cc 1.6 1.2 6 0	kV Units  Dilector  V  mA
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/It Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage,	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ  pull-up to +5  For full RS422 compliance, see <sup>2</sup> Hysteresis  Common-mode range	±5 Min.  TTL 1.8  1.4  0  ±0.06	3.3 4.7 5.5 0	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2	kV Units  Dilector  V
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see ²  Hysteresis  Common-mode range (A+ to GND, etc.)	±5 Min.  TTL /  1.8  1.4	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A	kV Units  Dilector  V  mA
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance,	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "HIGH"  Floating voltage (not connected)  Logic "HIGH"; Internal 2.2ΚΩ pull-up to +5  For full RS422 compliance, see <sup>2</sup> Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-	±5 Min.  TTL 1.8  1.4  0  ±0.06	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2	kV Units  Dilector  V  mA
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see ²  Hysteresis  Common-mode range (A+ to GND, etc.)	±5 Min.  TTL 1.8  1.8  1.4  0  ±0.06	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7	kV Units Ollector V mA V kΩ
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see ²  Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-  Z1+ to Z1-  Single-ended mode, Open-collector / NPN	±5 Min.  TTL 1.8  1.4  0  ±0.06	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2	kV Units  Units  V  V  mA
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance,	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "HIGH"  Floating voltage (not connected)  Logic "HIGH"; Internal 2.2ΚΩ pull-up to +5  For full RS422 compliance, see ²  Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-  Z1+ to Z1-  Single-ended mode, Open-collector / NPN  Differential mode, or Single-	±5 Min.  TTL 1.8  1.4  0  ±0.06  -7	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7	kV Units Ollector V  MA  V  kΩ  MHz
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential	B1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "HIGH" Floating voltage (not connected)  Logic "HIGH"; Internal 2.2ΚΩ pull-up to +5  For full RS422 compliance, see <sup>2</sup> Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-  Z1+ to Z1-  Single-ended mode, Open-collector / NPN  Differential mode, or Single-ended driven by push-pull	±5 Min.  TTL 1.8  1.8  1.4  0  ±0.06	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7	kV Units Ollector V mA V kΩ
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "HIGH"  Floating voltage (not connected)  Logic "HIGH"; Internal 2.2ΚΩ pull-up to +5  For full RS422 compliance, see ²  Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-  Z1+ to Z1-  Single-ended mode, Open-collector / NPN  Differential mode, or Single-	±5 Min.  TTL /  1.8  1.4  0  ±0.06  -7  0	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7	kV Units  Ollector  V  MA  V  kΩ  MHz  MHz
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode differential mode Input impedance, differential	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see <sup>2</sup> Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-  Z1+ to Z1-  Single-ended mode, Open-collector / NPN  Differential mode, or Single-ended driven by push-pull (TTL / CMOS)  Single-ended mode, Open-collector / NPN	±5 Min.  TTL 1.8  1.4  0  ±0.06  -7	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7	kV Units Ollector V  MA  V  kΩ  MHz
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "HIGH" Floating voltage (not connected)  Logic "HIGH"; Internal 2.2ΚΩ pull-up to +5  For full RS422 compliance, see <sup>2</sup> Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-  Z1+ to Z1-  Single-ended mode, Open-collector / NPN  Differential mode, or Single-ended driven by push-pull (TTL / CMOS)  Single-ended mode, Open-collector / NPN  Differential mode, or Single-ented mode, Open-collector / NPN	±5 Min.  TTL 1.8  1.8  1.4  0  ±0.06  -7  0  1	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7	kV Units  Dilector  V  MA  V  kΩ  MHz  MHz  μs
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential Input frequency  Minimum pulse	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see 2  Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1- Z1+ to Z1- Single-ended mode, Open- collector / NPN  Differential mode, or Single- ended driven by push-pull  (TTL / CMOS)  Single-ended mode, Open- collector / NPN  Differential mode, or Single- ended driven by push-pull	±5 Min.  TTL /  1.8  1.4  0  ±0.06  -7  0	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7	kV Units  Ollector  V  MA  V  kΩ  MHz  MHz
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential Input frequency  Minimum pulse	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"  Logic "HIGH"  Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see ²  Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-  Z1+ to Z1-  Single-ended mode, Opencollector / NPN  Differential mode, or Single-ended driven by push-pull (TTL / CMOS)  Single-ended mode, or Single-ended driven by push-pull (TTL / CMOS)	±5 Min.  TTL.  1.8  1.4  0  ±0.06  -7  0  1  50	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7 5 10	kV Units  Dilector  V  MA  V  kΩ  MHz  MHz  μs
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential Input frequency  Minimum pulse width	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see 2  Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1- Z1+ to Z1- Single-ended mode, Open- collector / NPN  Differential mode, or Single- ended driven by push-pull  (TTL / CMOS)  Single-ended mode, Open- collector / NPN  Differential mode, or Single- ended driven by push-pull	±5 Min.  TTL 1.8  1.8  1.4  0  ±0.06  -7  0  1	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7	kV Units  Units  Units  Units  V  V  MA  W  MHz  MHz  MHz  Ms  ns
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential Input frequency  Minimum pulse	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"; Pull to GND  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see 2  Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1- Z1+ to Z1- Single-ended mode, Open-collector / NPN  Differential mode, or Single-ended driven by push-pull (TTL / CMOS)  Single-ended mode, Open-collector / NPN  Differential mode, or Single-ended driven by push-pull (TTL / CMOS)  Absolute maximum values, continuous  Absolute maximum varge	±5 Min.  TTL /  1.8  1.4  0  ±0.06  -7  0  1  50  -7	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7 10	kV Units  Dilector  V  MA  V  kΩ  MHz  MHz  μs
ESD protection  Encoder1 Inputs (A1/A1+, A1-, B1/Is Single-ended mode compliance Input voltage, single-ended mode A/A+, B/B+ Input voltage, single-ended mode Z/Z+ Input current, single-ended mode A/A+, B/B+, Z/Z+ Differential mode compliance Input voltage, differential mode Input impedance, differential Input frequency  Minimum pulse width  Input voltage, any	S1+, B1-, Z1/Z1+, Z1-)  Leave negative inputs disconnected Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "LOW"  Logic "HIGH" Floating voltage (not connected)  Logic "HIGH" Floating voltage (not connected)  Logic "HIGH"; Internal 2.2KΩ pull-up to +5  For full RS422 compliance, see ²  Hysteresis  Common-mode range (A+ to GND, etc.)  A1+ to A1-, B1+ to B1-  Z1+ to Z1-  Single-ended mode, Open-collector / NPN  Differential mode, or Single-ended driven by push-pull (TTL / CMOS)  Single-ended mode, Open-collector / NPN  Differential mode, or Single-ended driven by push-pull (TTL / CMOS)  Absolute maximum values, continuous	±5 Min.  TTL.  1.8  1.4  0  ±0.06  -7  0  1  50	3.3 4.7 5.5 0 TIA/E ±0.1	/ Open-cc 1.6 1.2 6 0 IA-422-A ±0.2 +7 5 10	kV Units  Units  Units  Units  V  V  MA  W  MHz  MHz  MHz  Ms  ns

Digital Outp (OUT0, OUT OUT5) <sup>1</sup>	uts 1, OUT2/Error, OUT3/Ready, OUT4,	Min.	Тур.	Max.	Units	
Mode compliance			PNP	24V		
Default	Not supplied (+VLOG floating or to GND)	High-Z (floating)				
state	Normal operation		Logic "	High"		
	Logic "HIGH"; output current = 0.2A		V <sub>LOG</sub> -0.2	V <sub>LOG</sub> -0.8		
Output	Logic "LOW"; output current = 0, no load	open-collector		or		
voltage	Logic "HIGH", external load to GND		0		V	
	Absolute maximum, continuous	-0.3		V <sub>LOG</sub> +0.3		
	Absolute maximum, surge (duration ≤ 1s) <sup>†</sup>	-0.5		V <sub>LOG</sub> +0.5		
	Logic "HIGH", source current, continuous			0.2	Α	
Output current	Logic "HIGH", source current, pulse ≤ 5 s			0.4	Α	
	Logic "LOW", means High-Z				mA	
Minimum pulse width		2			μs	
ESD protection	Human body model	±15			kV	

Mode compliance			NPN	24V	
Default	Not supplied (+VLOG floating or to GND)	(+VLOG floating or Hi		High-Z (floating)	
state	Normal operation		Higl	n-Z	
Output voltage	Logic "LOW"; output current = 0.3A		0.2	8.0	
	Logic "HIGH"; output current = 0, no load	open-collecto		tor	
	Logic "HIGH", external load to +V <sub>LOG</sub>		$V_{LOG}$		V
	Absolute maximum, continuous	-0.3		V <sub>LOG</sub> +0.3	
	Absolute maximum, surge (duration ≤ 1s)	-0.5		V <sub>LOG</sub> +0.5	
0.1.1	Logic "LOW", sink current, continuous			0.3	Α
Output current	Logic "LOW", sink current, pulse ≤ 5 s			0.5	Α
	Logic "HIGH", means High-Z				mA
Minimum pulse width		2			μs
ESD protection	Human body model	±15			kV

	· ,			Max.	Units
Differential mode compliance	mode		TIA/EI/	4-422-A	
	Hysteresis	±0.06	±0.1	±0.2	
Input voltage	Differential mode	-14		+14	V
	Common-mode range (A+ to GND, etc.)	-11		+14	
Input impedance, differential	A2+, B2+, Z2+ A2-, B2-, Z2-		150		Ω
Input frequency	Differential mode	0		10	MHz
Minimum pulse width	Differential mode	50			ns

 $<sup>^3</sup>$  Encoder2 differential input pins have internal 150  $\!\Omega$  termination resistors connected across

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<sup>&</sup>lt;sup>1</sup> The digital inputs and outputs are software selectable as PNP or NPN

The digital injuits and outputs are software selectable as THY OTNEY of The OTNEY o



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Sin-Cos Encoder (Sin+, Sin-, Cos+,		Min.	Тур.	Max.	Units	
Input voltage, differential	Sin+ to Sin-, Cos+ to Cos-		1	1.25	V <sub>PP</sub>	
	Operational range	-1	2.5	4		
Input voltage, any	Absolute maximum values, continuous	-7		+7	V	
pin to GND	Absolute maximum, surge (duration ≤ 1s)	-11		+14		
Input impedance	Differential, Sin+ to Sin-, Cos+ to Cos- 1	4.2	4.7		kΩ	
input impedance	Common-mode, to GND		2.2		kΩ	
Resolution with interpolation	Software selectable, for one sine/cosine period	2		10	bits	
Frequency	Sin-Cos interpolation			450	kHz	
	Quadrature, no interpolation	0		10	MHz	
ESD protection Human body model		±1			kV	
Analog 05V Inp		Min.	Тур.	Max.	Units	
	Operational range Absolute maximum values,	-12		5 +18	l	
Input voltage	Absolute maximum, surge			±36	V	
Innert inner der	(duration ≤ 1s)		00		1:0	
Input impedance Resolution	To GND		28		kΩ	
Integral linearity			12	±2	bits bits	
Offset error			±2	±10	bits	
Gain error			±1%	±3%	% FS <sup>2</sup>	
Bandwidth (-3Db)	Software selectable	0	±170	1	kHz	
ESD protection	Human body model	±5		- '	kV	
RS-232	Truman body model	Min.	Turn	Max.	Units	
		IVIIII.	Typ.	-232-C	Ullits	
Compliance	0.6	0000	HA/EIA			
Bit rate Short-circuit	Software selectable	9600 115200 Baud Guaranteed				
	232TX short to GND		Guara	inteea	kV	
Safe torque OFF	Human body model	±2				
(STO1+, STO1-, S	TO2+, STO2+)	Min.	Тур.	Max.	Units	
Safety function	According to EN61800-5-2	S	TO (Safe 1	orque OF	F)	
EN 61800-5-1/ -2	Safety Integrity Level					
and EN 61508-5- 3/ -4 Classification	PFHD (probability of dangerous failures per hour)	safety integrity level 3 (SIL3)  8*10 <sup>-10</sup> hour <sup>-1</sup> (0.8 FIT)				
Olassilloation	Performance Level		Cat3	/DI o		
EN13849-1 Classification	MTTFM (meantime to dangerous failure)	Cat3/PLe 377 yea			years	
Mode compliance	uangorous railars)	PNP				
Default state	Input floating (wiring disconnected)		Logic	LOW		
	Logic "LOW"	-20		5.6		
lanci de contida aca	Logic "HIGH"	18		36	1 .,	
Input voltage	Absolute maximum, continuous	-20		+40	V	
Input voltage Input current	Absolute maximum,		0 5		V mA	
Input current Repetitive test	Absolute maximum, continuous Logic "LOW"; pulled to GND			+40	mA ms	
Input current	Absolute maximum, continuous Logic "LOW"; pulled to GND Logic "HIGH", pulled to +Vlog			+40	mA	
Input current Repetitive test pulses	Absolute maximum, continuous Logic "LOW"; pulled to GND Logic "HIGH", pulled to +Vlog			+40 13 5	mA ms	
Input current Repetitive test pulses (high-low-high) Fault reaction	Absolute maximum, continuous Logic "LOW"; pulled to GND Logic "HIGH", pulled to +Vlog Ignored high-low-high  From internal fault detection to register DER bit 14 = 1 and			+40 13 5 20	mA ms Hz	

ernet	61158-3/4/	5/6-12)		
	1 4 0 0 D 4 C			
EEE	Fast Ethernet 100BASE- (IEEE802.3u)			
Auto-negotiation for 100Mbps duplex				
etec	t MDI/MDI	-X		
compliant to IEEE802.3af mode A "Mixed DC & Data"				
NOT compliant to IEEE802.3af mode B "DC on Spares"				
		$V_{rms}$		
		$kV_{peak}$		
50		m		
		kV		
100	letec o IEE ed D olian B "Do	tion for 100Mb duplex letect MDI/MDI D IEEE802.3af ed DC & Data" bliant to IEEE8 B "DC on Spare		

When the connections between drives are done directly, without magnetics (nonstandard, not conform to Ethernet IEEE802.3 100BASE-TX), it is imperative that the ground voltage difference between drives is kept to a minimum. The installation must provide a supplementary GND link between the drives. This link must have low inductance. Low inductance is best achieved by using large metal parts, such as a metallic chassis / baseplate, or using copper conductive tape.

LED signals		Min.	Тур.	Max.	Units	
LED		Common cathode to GND				
connection		Direct, no series resistor				
LED current	LED current			0.7	1	mA
Conformity	Conformity		Min.	Тур.	Max.	Units
	2014/30/EU (E 2014/35/EU (L					

	oundining			ιyp.	max.	Oiiita
	EU Declaration	2014/30/EU (EMC), 2014/35/EU (LVD), 2011/65/EU (RoHS), 1907/2006/EC (REACH),		-76-		
Ľ	Scolaration	93/68/EEC (CE Marking Directive), EC 428/2009 (non dual-use item, ou	ıtput frequ	uency limi	ted to 590	)Hz)

<sup>†</sup> Stresses beyond values listed under "absolute maximum ratings" may cause permanent damage to the device. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

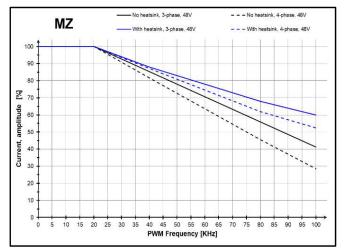
 $^1$  For many applications, a 120 $\Omega$  termination resistor should be connected across SIN+ to SIN-, and across COS+ to COS-. Please consult the feedback device datasheet for confirmation.

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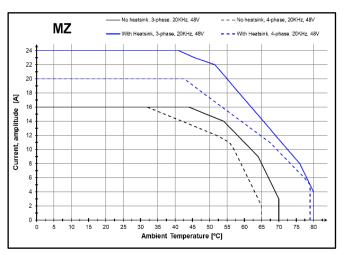


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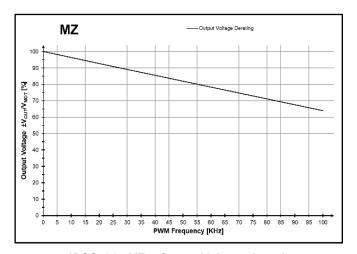




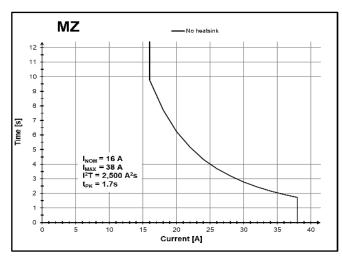
iPOS481x MZ – Current de-rating with PWM frequency, @48V



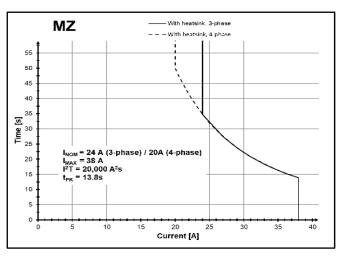
iPOS481x MZ – Current de-rating with ambient temperature



iPOS481x MZ – Output Voltage de-rating with PWM frequency



iPOS481x MZ – Over-current diagram (No heatsink)



iPOS481x MZ – Over-current diagram (With heatsink)

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